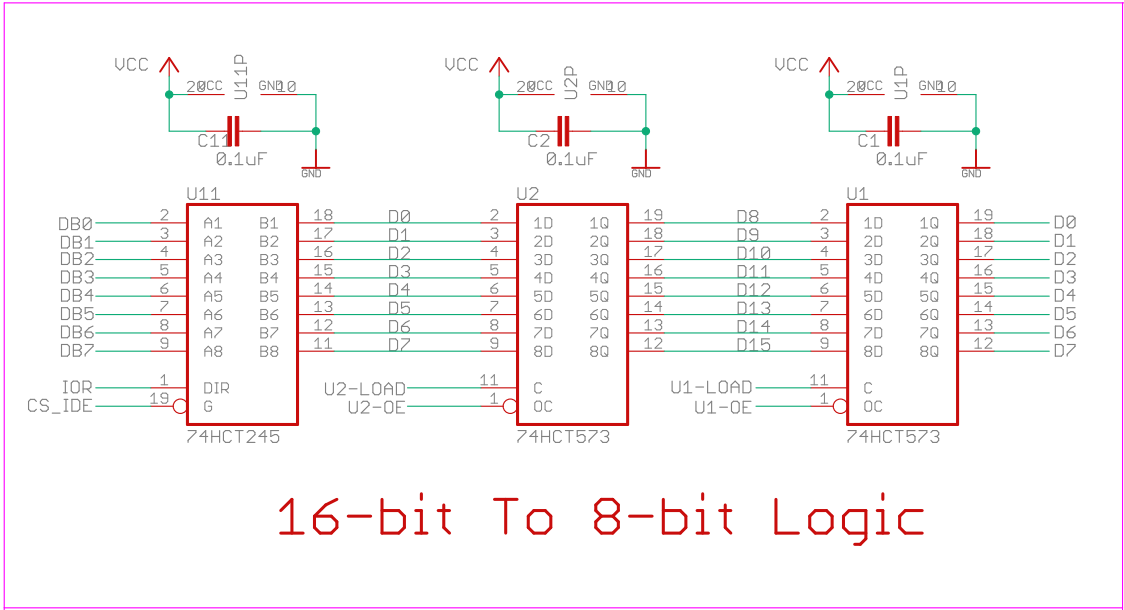
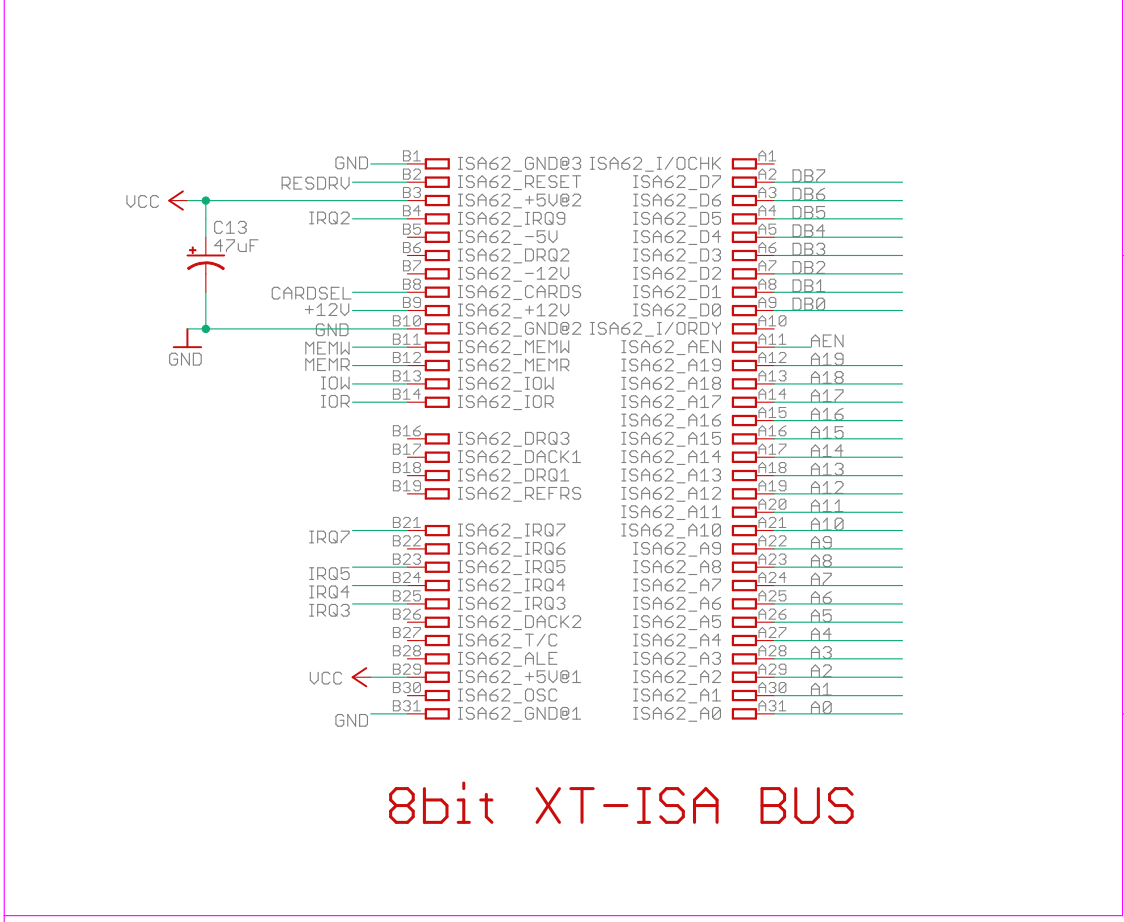
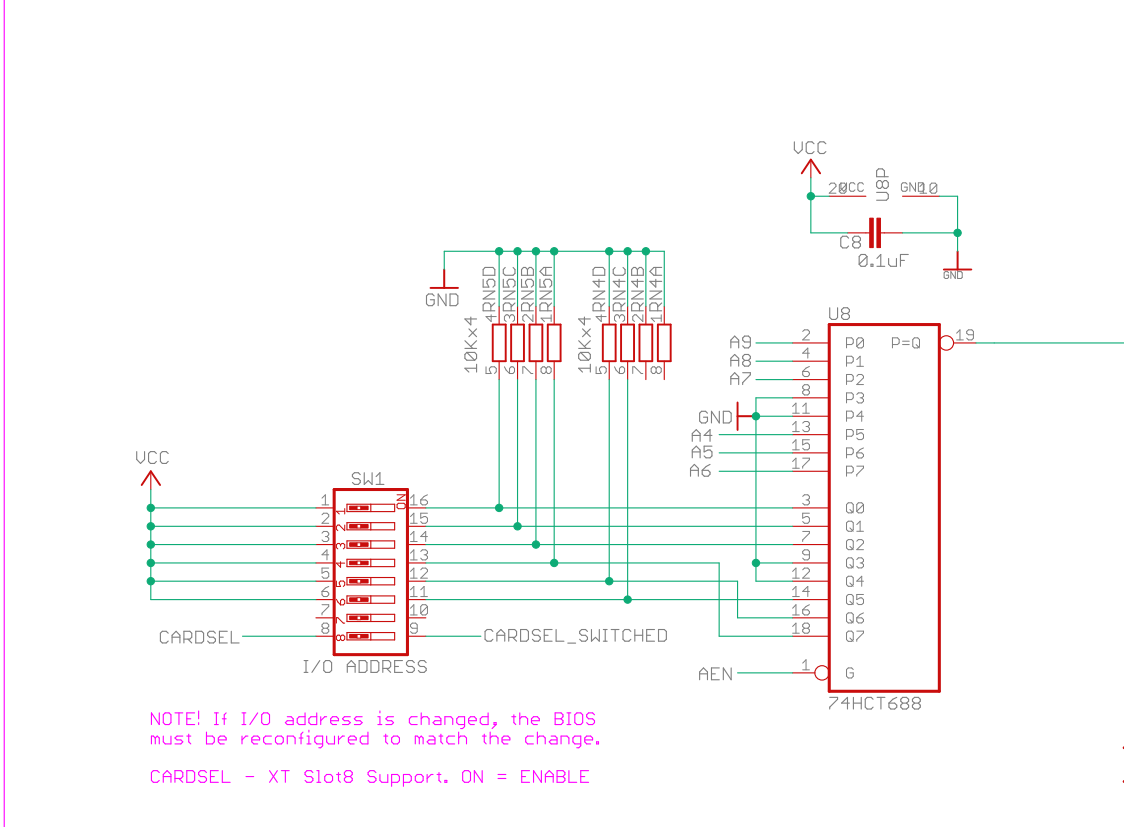




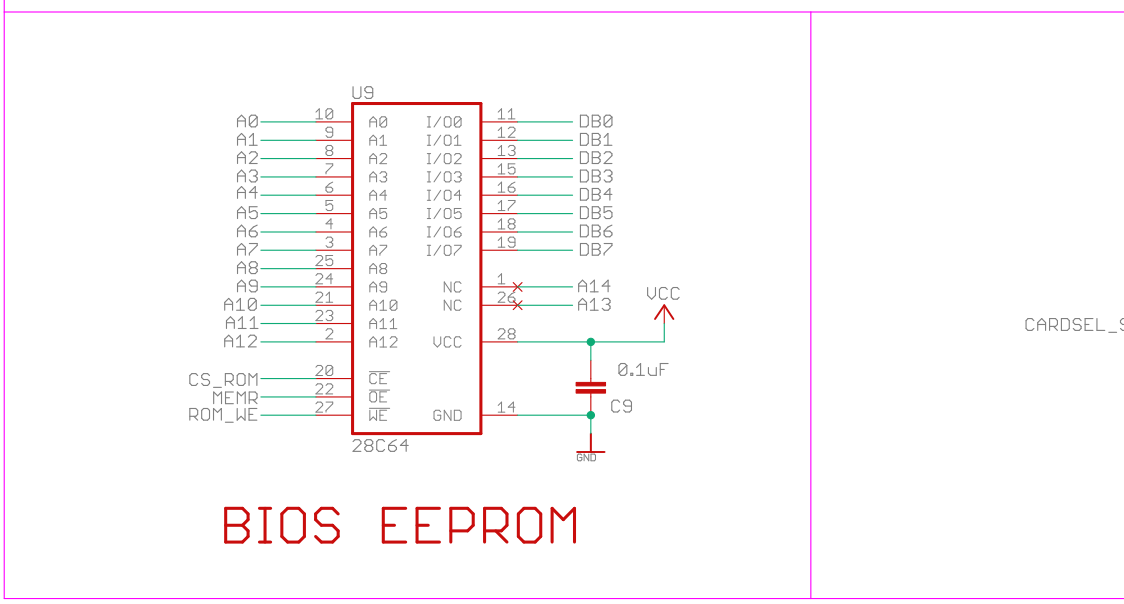
16-bit To 8-bit Logic



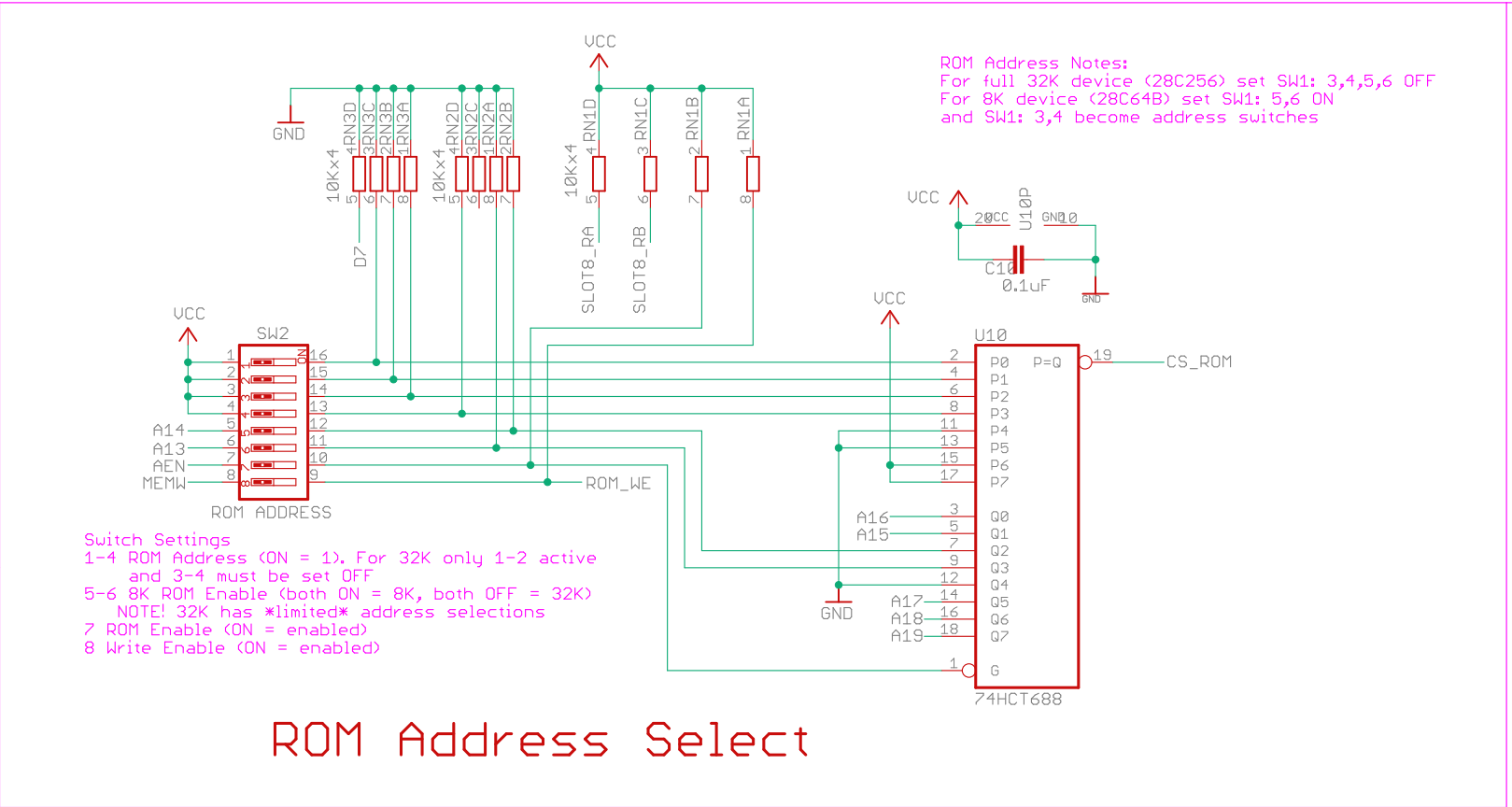
8bit XT-ISA BUS



NOTE! If I/O address is changed, the BIOS must be reconfigured to match the change.
CARDSSEL - XT Slot8 Support. ON = ENABLE

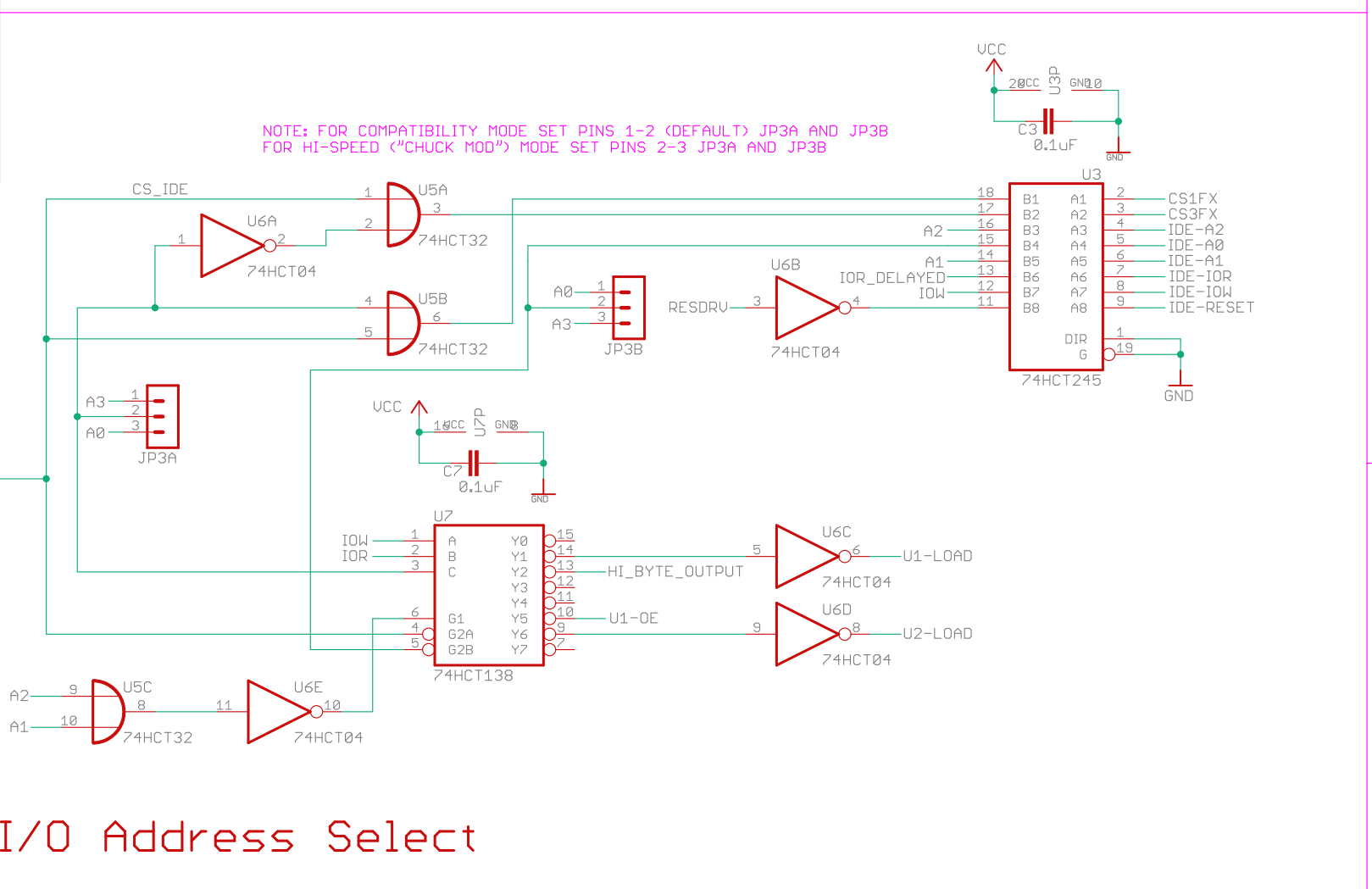


BIOS EEPROM

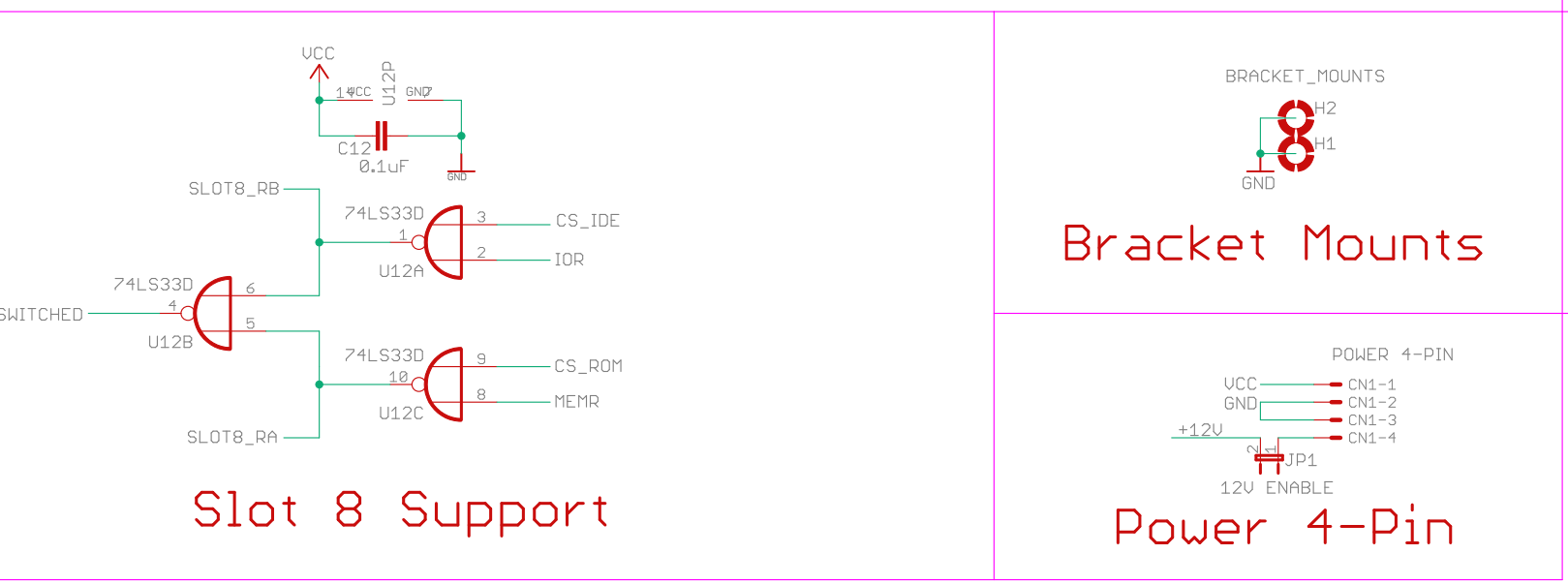


Switch Settings
1-4 ROM Address (ON = 1). For 32K only 1-2 active and 3-4 must be set OFF
5-6 8K ROM Enable (both ON = 8K, both OFF = 32K)
NOTE! 32K has *limited* address selections
7 ROM Enable (ON = enabled)
8 Write Enable (ON = enabled)

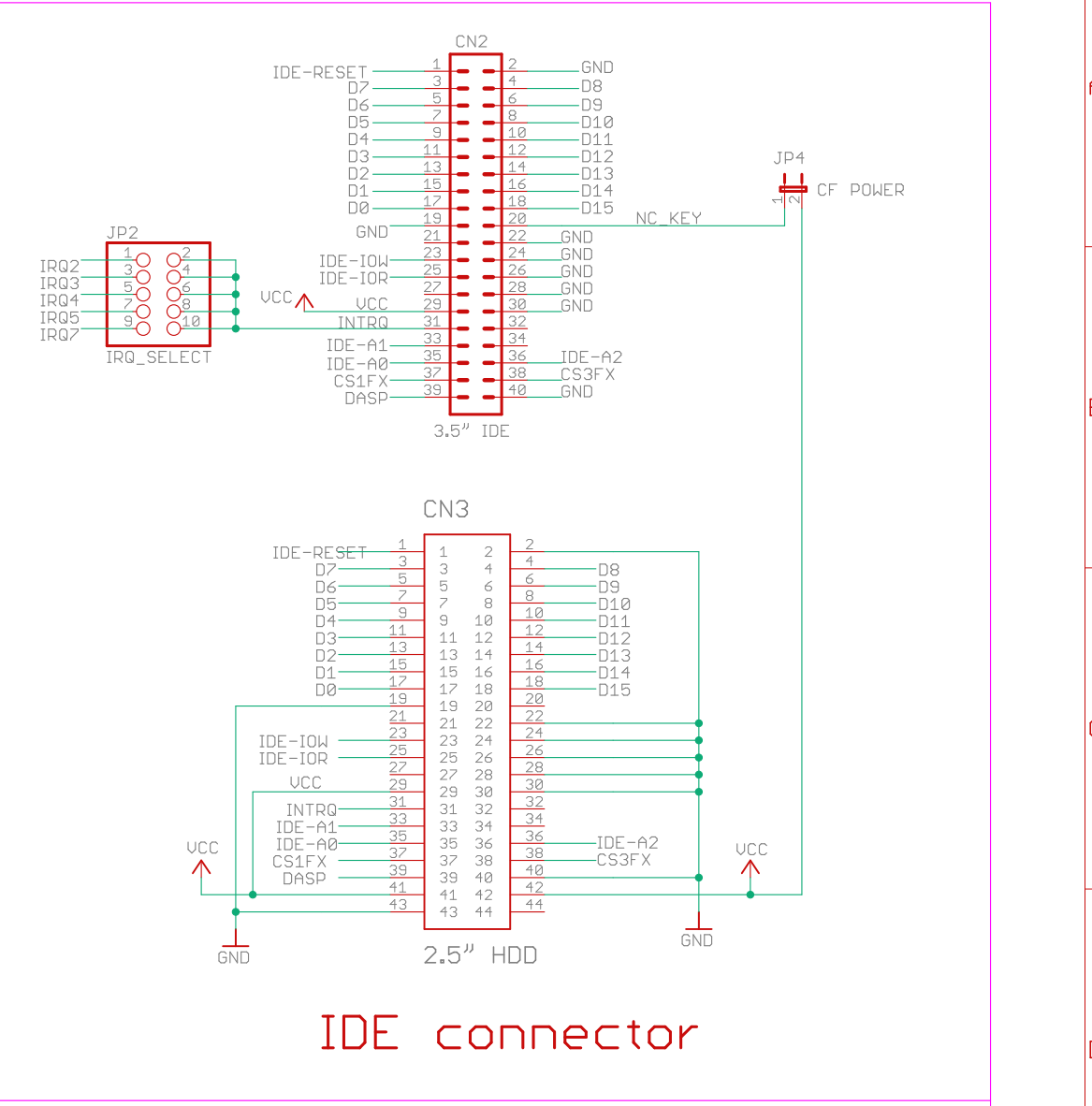
ROM Address Select



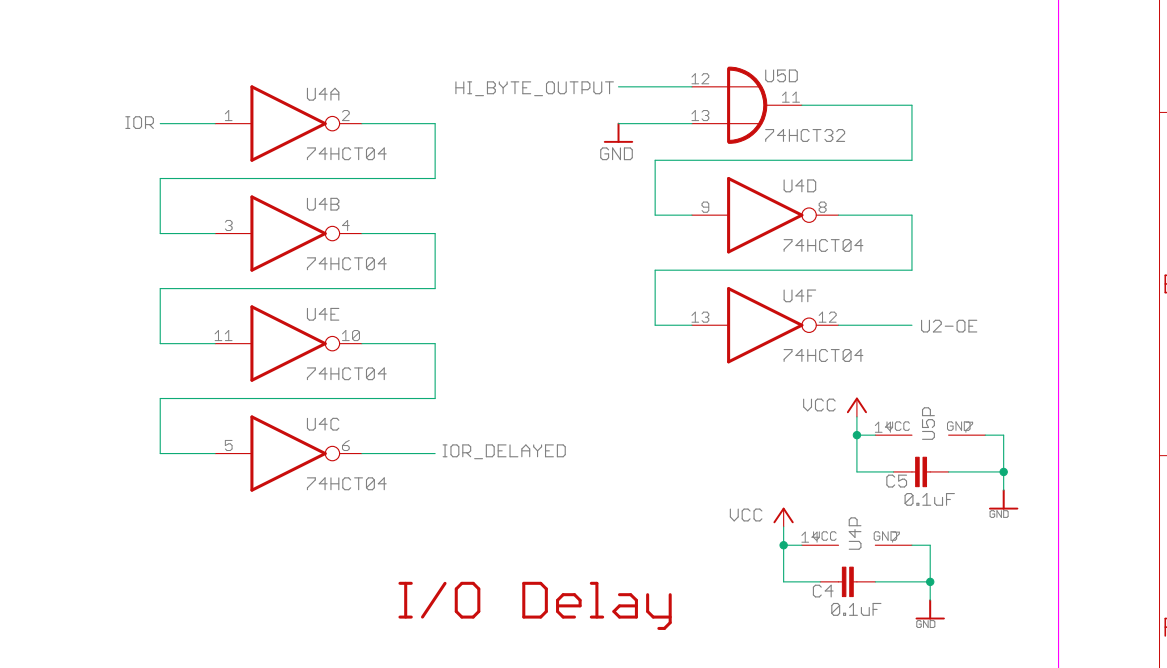
I/O Address Select



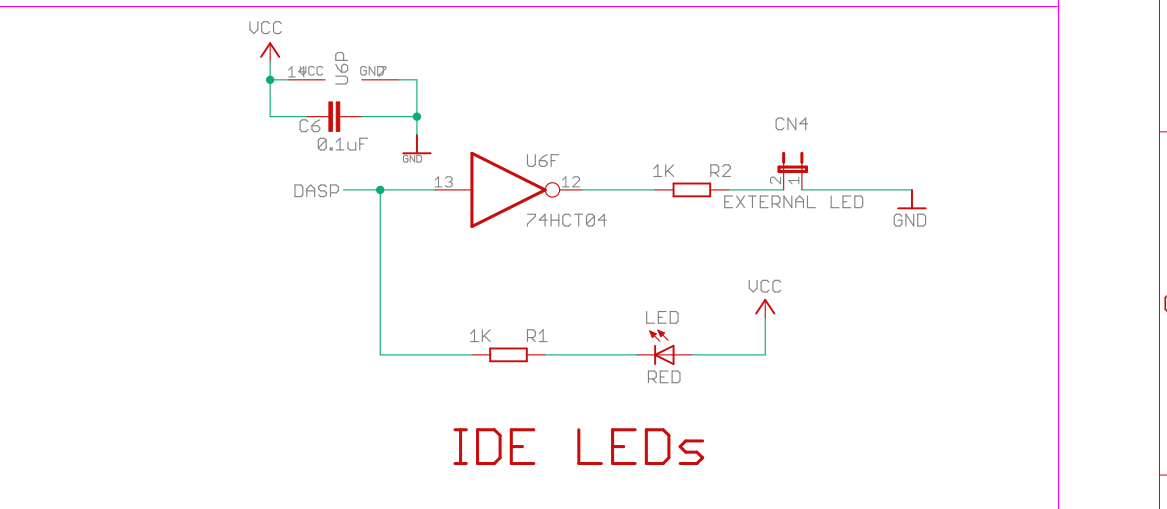
Slot 8 Support



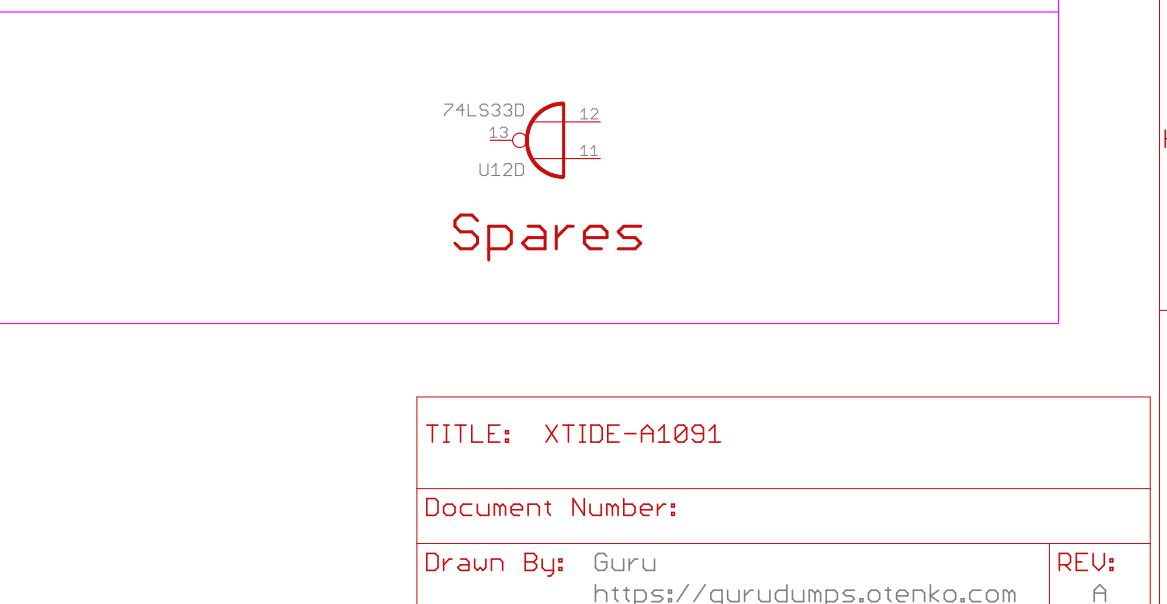
IDE connector



I/O Delay

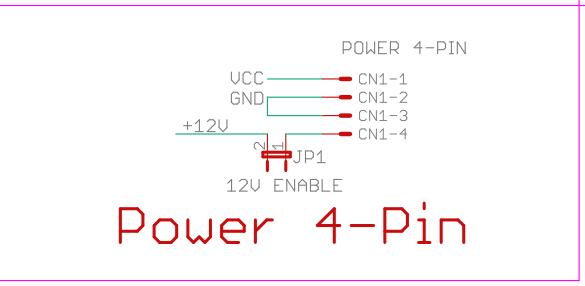


IDE LEDs



Bracket Mounts

Spares



Power 4-Pin